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oscilloscope signal of proj

MING CHI UNIVERSITY OF TECHNOLOGY

FA and signal quality analysis for Server Hardware Engineering

- 1. FA (Failure analysis) job
- 2. Low speed bus signal measurement
- 3. Rework for R&D



I belong to Product Development Division 1 under the Server Business Unit II. In technology support team, we are responsible for backing up EE design tasks, including signal measurement, signal quality verification, analyzing failure project PCBA as return from customer (HP), and reworking for Hardware Engineer. Figure 1 shows the power signal measurement situation.



Fig. 1 The power signal measurement

Failure analysis

FA is a job diagnosing failure PCBA return from customer. We learn problem analysis as well as FA writing, and we need to bring this failed unit back for EE validation. Some information about these jobs is shown in Figs. 2 to 4 and Table 1. (Fig. 2 & Fig. 3 and Fig. 4 & Table 1 show two different kinds of tasks.)



Fig. 4 Dismount a connector from M/B



Fig. 2 The failure main board



Fig. 3 Failure details and description

Table 1 The comparison between the signals of two main boards

M/B	Failure board	Good board
Measure at	CON_TXD&CON_RXD	CON_TXD&CON_RXD
Signal	An appropriate to the part of	Compared Agency Says Depth Court Property Court State

Low speed bus signal measurement (Signal Integrity)

In order to verify the signal integrity of the lower speed buses on the cartridge, we list all buses (Ex: 1²C & LPC) and relevant parameters, test for all conditions of master/slave. Then, the next procedure is used oscilloscope to capture rise, fall, and timing. Figure 5 shows the 1²C-bus signal of project board, including "DATA" and "CLK".

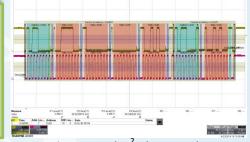


Fig. 5 Decode I²C-bus signal

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