明志 MING CHI U	IO5 四技部工讀實務實習 KH 技大學 成果海報
工作 項目	Basic Input Output System (BIOS) Maintain Project
內容 摘要	1. 訓練課程3. 專案維護2. 客製修改4. 新案開發
	修改 GPIO function mode: Step1 將 Chipset 目前 GPIO 配置 dump 並顯示出來: 使用 intel 的 GPIO Tool,在 Shell 下輸入 Commond line: GpioConfig.efi –r Default_GPIO.bin, 儲存目前 GPIO 配置並將名稱存為"Default_GPIO.bin "
	用文字編譯器或是記事本,將裡頭的 GPIO 配置顯示出來 /* CFGO Register 0 */ /* Platform=CherryTrail; */ 0x4400 0x00118102 /*GPIO_NAME = GPIO_N_00*/ 0x4408 0x00118102 /*GPIO_NAME = GPIO_N_01*/ 0x4410 0x00918102 /*GPIO_NAME = GPIO_N_02*/
實	以 GPIO_N_00 為例,前面的 0x4400 為 MMIO Address,後面 0x00118102 為此位址的資料 由 Code 得知 GPIO_N_00 的 Base Address = 0xFED8C400。
習 成 果	Datasheet 中可查到每個腳位的意義,而 bit 15 為 GPIO Enable (0: Native , 1: GPIO) Bit Range Default and Access 15 0x0 RW GPIO Enable (GPIOEn): This enables GPIO mode and function in Pad Mode no longer fields control the Pad. 0x0 RW GPIO Enable (GPIOEn): This enables GPIO mode and function in Pad Mode no longer (0) = GPIO disabled and function defined in Pad Mode controls the pad (0) = GPIO disabled and Pad Register controls the pad
	<pre>Step2 將 GPIO_N_00 腳位修改成 Native mode: 在 OemBoardGpio.h 中找到 AMI_GPIO_TABLE, 並將 GPIO_N_00 改成 Native // North Community CHV_GPIO_PAD_INIT mBSW_CH_GpioInitData_N[] = // Group Pin#: PAD Name Mode Select Mode# GPIO Config GPIO IX State { CHV_GPIO_PAD_CONF (L"N37: CX_PRDY_B ", Native, M1, HI_2, NA, CHV_GPIO_PAD_CONF (L"N55: GP CAMERASB11 ", GPIO, M1, GPIO, LOW, CHV_GPIO_PAD_CONF (L"N00: GPIO DFX0 ", GPIO, M1, GPO, HIGH, HIGH, CHV_GPIO_PAD_CONF (L"N03: GPIO_DFX1 ", GPIO, M1, GPO, HIGH, HIGH, GPIO, HIGH, CHV_GPIO_PAD_CONF (L"N03: GPIO_DFX1 ", GPIO, M1, GPO, HIGH, HIGH, GPIO, HIGH, CHV_GPIO_PAD_CONF (L"N03: GPIO_DFX1 ", GPIO, M1, GPO, HIGH, GPO, HIGH, GPIO, M1, GPO, HIGH, HIGH, GPIO, M1, GPO, HIGH, GPIO, HIGH, GPIO, M1, GPIO, HIGH, GPIO, HIGH, GPIO, M1, GPIO, HIGH, HIGH, GPIO, M1, GPIO, HIGH, GPIO, GPIO, GPIO, GPIO, GPIO, GPIO, GPIO, M1, GPIO, HIGH, GPIO, HIGH, GPIO, HIGH, GPIO, GPIO, GPIO, GPIO, GPIO, GPIO, M1, GPIO, HIGH, GPIO, HIGH, GPIO, HIGH, GPIO, M1, GPIO, HIGH, GPIO, HIGH, GPIO, HIGH, GPIO, M1, GPIO, M1, GPIO, GPIO, GPIO, GPIO, GPIO, GPIO, GPIO, M1, GPIO, GPIO, GP</pre>
	<pre>// North Community CHV_GPIO_PAD_INIT mBSW_CH_GpioInitData_N[] = // Group Pin#: PAD Name Mode Select Mode# GPIO Config GPIO TX State { CHV_GPIO_PAD_CONF (L"N37: CX_PRDY_B ", Native , M1 , HI_Z , NA ,</pre>
	Step3 結果: 00 03020100 00 03020100 00 03020100 可以使用 RU Tool 00 00118102 60 03020100 00 03020100 確認目前的狀態 00 00118102 60 00110102 60 00110102
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